

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-2. (canceled)

Claim 3 (previously presented) An integrated circuit in a communications terminal device, comprising:

a microcontroller;

a radio-cell specific logic module;

a digital signal processor for digital voice processing; and

an interface to a digital voice memory with which a call-answering functionality is enabled via the microcontroller in combination with the digital voice memory.

BI Claim 4. (previously presented) An integrated circuit in a communications terminal device as claimed in claim 3, wherein call-answering software is deposited in the microcontroller.

Claim 5. (previously presented) An integrated circuit in a communications terminal device as claimed in claim 3, wherein the microcontroller, the radio cell-specific logic module and the digital signal processor are connected to one another via an internal bus system.

Claim 6. (previously presented) An integrated circuit in a communications terminal device as claimed in claim 5, wherein the digital voice memory also is connected to the internal bus system.

Claim 7. (previously presented) A mobile radio device for wireless linking to a cellular radio network according to the DECT standard, comprising:

an integrated circuit having a microcontroller, a radio cell-specific logic module, a digital signal processor for digital voice processing, and an interface to a digital voice memory with

which a call-answering functionality is enabled via the microcontroller in combination with the digital voice memory; and

a receptacle device connected to the integrated circuit for acceptance of the digital voice memory.

Claim 8. (previously presented) A mobile radio device for wireless linking to a cellular radio network as claimed in claim 7, wherein call-answering software is deposited in the microcontroller.

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Claim 9. (previously presented) A mobile radio device for wireless linking to a cellular radio network as claimed in claim 7, wherein the microcontroller, the radio cell-specific logic module and the digital signal processor are connected to one another via an internal bus system.

Claim 10. (previously presented) A mobile radio device for wireless linking to a cellular radio network as claimed in claim 9, wherein the digital voice memory also is connected to the internal bus system.
